

## **Designing an IC- Becoming a Power User**

To remain competitive in this tight job market, it is essential for all Semiconductor professionals/ IC Design Engineers to “sharpen their skill set”. We recently launched up a startup "Catalyte IC Design"- one of our primary focus is "Trainings on High-end IC design". CatalyteIC will offer a short seminar in Austin, Texas next Tue (6/2). Here's the information for the seminar.

What- Designing an IC- Becoming a Power User

When- June 2<sup>nd</sup> (Tue), 8:30pm – 9:30pm

Where- One Highland Center (4th floor), 314 E. Highland Mall Blvd, Austin, TX 78752

### **Intended Audience**

1. Anyone involved with some phases of IC design (e.g. - Verification, RTL, Synthesis, Physical design, and Layout, Analog and RF design, EDA, PDK) - Expand understanding with **all phases** of the design flow.
2. Anyone involved with semiconductor industry (e.g. - Design, Process, Product and Test Engineering, Library/PDK) - Widen the Horizon

### **Outline of the Presentation**

- Company overview- Catalyte IC Design
- Module1- Designing an IC- Concepts through Implementations
- Module2- Custom Design Flow– Default to Power
- Q/A

### **Presenter**

Faizul Alam  
Chief Technology Officer  
Catalyte IC Design

For additional info-  
[www.catalyteic.com](http://www.catalyteic.com)